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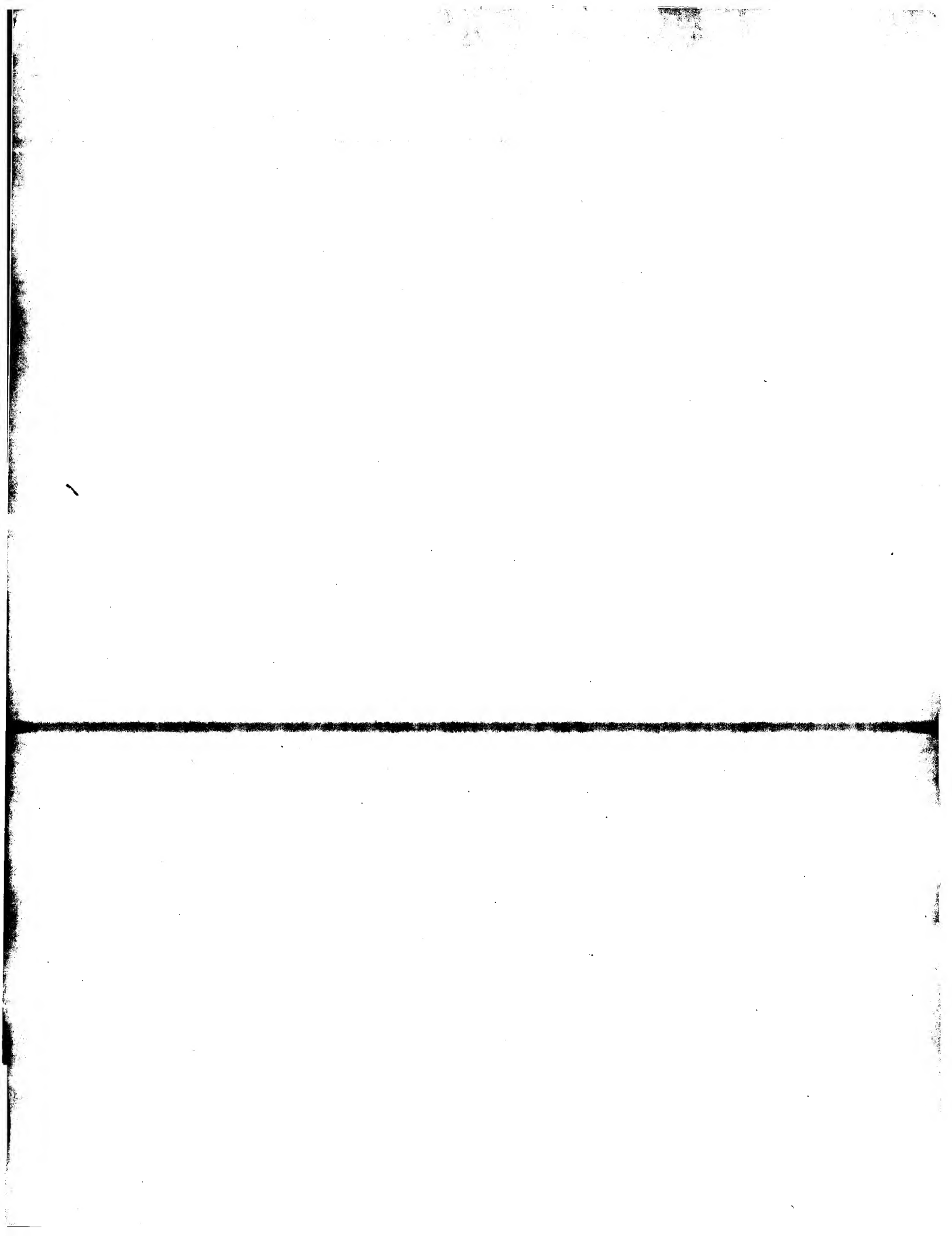
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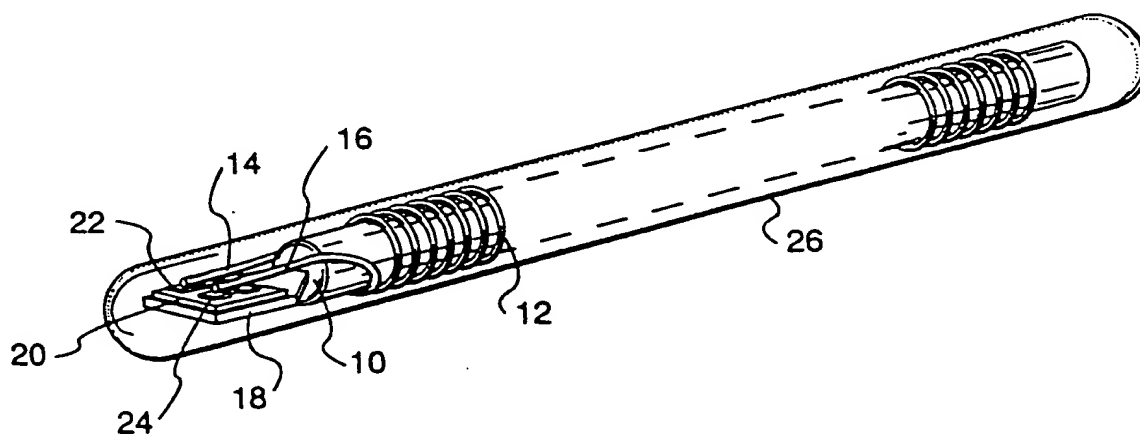
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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

<p>(51) International Patent Classification⁵ : G01S 13/80, H01L 21/447, 21/56 H01L 23/06, H01Q 1/40</p>	<p>A1</p>	<p>(11) International Publication Number: WO 92/22827 (43) International Publication Date: 23 December 1992 (23.12.92)</p>
<p>(21) International Application Number: PCT/US92/04731 (22) International Filing Date: 5 June 1992 (05.06.92) (30) Priority data: 710,786 5 June 1991 (05.06.91) US 787,828 5 November 1991 (05.11.91) US (71) Applicant: TROVAN LIMITED [GB/US]; 2535 Sycamore Canyon Road, Santa Barbara, CA 93108 (US). (72) Inventors: ZIRBES, Glen, Leo ; RR #1 Box 216, Silver Lake, MN 55381 (US). HADDEN, Leonard, D. ; 4029 Lyndale Avenue South, Minneapolis, MN 55409 (US).</p>	<p>(74) Agent: HAMRICK, Claude, A., S.; Rosenblum, Parish & Isaacs, 160 West Santa Clara Street, 15th Floor, San Jose, CA 95113 (US). (81) Designated States: AT (European patent), AU, BE (European patent), CA, CH (European patent), DE (European patent), DK (European patent), ES (European patent), FR (European patent), GB (European patent), GR (European patent), IT (European patent), JP, KR, LU (European patent), MC (European patent), NL (European patent), SE (European patent). Published With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</p>	

(54) Title: AN IMPROVED MINIATURE TRANSPONDER DEVICE



(57) Abstract

A method and apparatus for facilitating interconnection of antenna lead wires (14, 16) to an integrated circuit (20) and encapsulating the assembly to form an improved miniature transponder device including the provision of an additional protective layer of insulation to the top of an integrated circuit chip or die and the provision of enlarged plated electrodes (22, 24) to the surface of the additional insulation to form enhanced bonding pads, such pads being electrically connected through the protective layers to the normal bonding pads of the integrated circuit device. The enhanced bonding pads (22, 24) are made of a soft conductive metal such that external wires (14, 16) to be attached thereto can be bonded to the pads using a thermal compression technique. This invention also extends to a method of encapsulating a transponder in heat shrunk plastics material.

1
2 Specification3 "AN IMPROVED MINIATURE TRANSPONDER DEVICE"
4
56 BACKGROUND OF THE INVENTION7 Field of the Invention

8 The present invention relates generally to the
9 manufacture of miniature electronic devices and more
10 particularly to a method and apparatus for facilitating
11 the attachment of electro-magnetic antenna wire leads to
12 an integrated circuit chip used in a miniature transponder
13 device suitable for use in a wide variety of applications
14 including implantation in a living animal. This invention
15 further relates to a method of protecting the miniature
16 transponder.
17

18 Brief Description of the Prior Art

19 As pointed out in the above-identified patent and
20 copending application the disclosures of which are hereby
21 incorporated into this application by references,
22 miniature passive transponders of the type used for object
23 identification, and particularly those which are
24 implantable into living creatures, such as livestock, are
25 very small and have inherent size restrictions that must
26 be considered in their design and manufacture. Most such
27 devices include a wire-wound electromagnetic antenna
28 electrically connected to an integrated circuit which, in
29 response to received transmitted energy obtained from the
30 antenna, generates a response signal which is
31 retransmitted to and through the antenna to a nearby
32 sensor.

33 - Heretofore, the integrated circuit was first mounted
34 to a metal leadframe, potted, and then the potted device
35 having leadframe leads extending therefrom was attached to
36 the antenna by bonding the antenna wires to the leads.
37 The necessity of providing a leadframe and assembling the
38 semiconductor die to the leadframe not only adds to the
39 cost of the device but also has a substantial bearing on

1 the minimum size to which a particular device may be
2 reduced.

3 In addition, it is necessary to protect the passive
4 transponder from exterior factors such as corrosive
5 environments and mechanically destructive impacts. This
6 is particularly so when the transponder is small and
7 delicate.

8 However, it is exactly because of the small and
9 delicate nature of the transponders in question that
10 adequate protection is not easy to apply. The need is
11 therefore for a simple and efficient means for protecting
12 the transponder which, when the protection is applied,
13 does not harm the transponder in any way.

14 15 SUMMARY OF THE PRESENT INVENTION

16 Briefly, a preferred embodiment of the present
17 invention includes the provision of an additional
18 protective layer of insulation to the top of an integrated
19 circuit chip or die and the provision of enlarged plated
20 electrodes to the surface of the additional insulation to
21 form enhanced bonding pads, such pads being electrically
22 connected through the protective layers to the normal
23 bonding pads of the integrated circuit device. The
24 enhanced bonding pads are made of a soft conductive metal
25 such that external wires to be attached thereto can be
26 bonded to the pads using a thermal compression bonding
27 technique.

28 This invention further extends to a method of
29 protecting a transponder by inserting such transponder in
30 a suitable heat shrinkable material and shrinking such
31 material over the transponder.

32 An important advantage of the present invention is
33 that it reduces the cost and size of a passive transponder
34 device.

35 Another advantage of the present invention is that it
36 makes possible visible inspection of all circuit
37 connections.

38 These and other advantages of the present invention
39 will no doubt become apparent to those skilled in the art

1 after having read the following detailed description of
2 the preferred embodiment which is illustrated in the
3 several figures of the drawing.

4

5

IN THE DRAWING

6

Fig. 1 is a perspective view illustrating an
7 encapsulated transponder apparatus illustrating one
8 application of the present invention;

9

Fig. 2 is a perspective view more clearly
10 illustrating a preferred embodiment of the present
11 invention;

12

Fig. 3 is a transverse cross-section taken in the
13 plane 3-3 of Fig. 2; and

14

Figs. 4 and 5 illustrate the method of protecting the
15 transponder apparatus of Fig. 1.

16

17

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

18

Referring now to Fig. 1 of the drawing, there is
19 shown a small implantable microtransponder device of the
20 type generally described in the above-mentioned copending
21 application but incorporating the present invention.

22

More specifically, the illustrated device includes an
23 electromagnetic antenna formed of an elongated cylindrical
24 magnetic core 10 having a length of ultra-fine conductive
25 wire 12 wrapped thereabout with the ends 14 and 16
26 extending forwardly. One such antenna is disclosed in
27 copending U.S. Patent Application Serial No. 400,600,
28 filed August 30, 1989, and assigned to the assignee of the
29 present invention. Affixed to the end of core 10 is a
30 support means 18 to which is attached an integrated
31 circuit die 20 having a pair of contact pads 22 and 24
32 provided thereon in accordance with the present invention.
33 As depicted, the wire end 14 is conductively bonded to pad
34 22, and the wire end 16 is conductively bonded to pad 24.
35 For some applications it may be possible to dispense with
36 the support means 18 and depend entirely upon the wires 14
37 and 16 for support prior to subsequent encapsulation.

38

Note that since the pads 22 and 24 are installed
39 during the wafer fabrication process, the only post-fab

1 testing that need be undertaken is that relating to the
2 bonding of wires 14 and 16 to the pads 22 and 24
3 respectively, and this can be easily accomplished using
4 automated test equipment. In other words, the usual
5 testing of chip-to-leadframe connection has been
6 eliminated. After assembly and test, the transponder
7 assembly is encapsulated in a suitable glass or plastic
8 capsule 26 and is at this point ready for implantation in
9 an animal or other object to be identified.

10 As described in the above-mentioned copending,
11 application, the die 20 includes electronic memory and
12 associated modulation circuitry such that in response to
13 power input thereto from the coil 12 generates an
14 identifying signal which is returned to coil 12 for
15 retransmission to a detecting antenna.

16 Turning now to Fig. 2 of the drawing, the die 20 is
17 shown in enlarged detail to include a semiconductive
18 substrate 30 having a standard phosphorus-doped glass
19 layer 32 deposited thereon which is overcovered by a
20 second layer of silicon nitride insulation 34 applied by
21 plasma-enhanced deposition. The thickness of layer 34 is
22 at least 15,000 angstroms and provides both insulation and
23 structural protection for the underlying die. After
24 deposition, the layer 34 is masked and two contact areas
25 or holes 50 are etched through the layers 32 and 34 to
26 contact the ac+ and ac- pads (not shown) on the circuit
27 contained in die 20 beneath layer 32. With the contact
28 areas open, the wafer is ready for the fabrication of the
29 enhanced contact pads 22 and 24. The process starts with
30 the deposition of a field metal layer 36 of TiW
31 (approximately 90% Ti and 10% W) having a thickness of
32 approximately 2,000 angstroms. On top of this layer a
33 layer of pure gold of approximately 1,200 angstroms is
34 deposited.

35 The enhanced contact pads 22 and 24 are then
36 deposited to a thickness of approximately 25 microns of
37 gold or copper. For the particular application
38 illustrated, the pads 22 and 24 are approximately 16
39 milli-inches long and 6 milli-inches wide and provide

1 adequate surface area to which the fine copper wires 14
2 and 16 may be attached by soldering, thermal compression
3 bonding or welding. It should be noted that the thick
4 silicon nitride layer 34 is of particular importance to
5 the invention in that the pads 22 and 24 are plated
6 directly over the active circuit area, i.e. the layer 34
7 serves to protect against damage to the underlying
8 circuitry during the wire lead attachment process which
9 would otherwise render the assembly useless.

10 Referring now to Fig. 3 of the drawing, which is a
11 partially broken cross-section taken along the plane 3-3
12 of Fig. 2, the manufacturing process is further
13 illustrated, it being understood of course that the
14 illustrated die is not fabricated individually but is made
15 jointly with other die, forming a silicon wafer. After
16 fabrication and test, the die 20 is separated from the
17 other die of the wafer. As depicted, the wafer 40 from
18 which the die 20 is cut forms a substrate the upper
19 portion 42 of which has integrated circuit components
20 formed therein. Deposited on the usual upper surface of
21 the wafer and forming a standard electrical interconnect
22 pad 44 is a metallization layer 46. A supporting
23 leadframe is normally electronically connected to pads 44
24 by fine wires. However, in accordance with the present
25 invention no such leadframe and connecting wires are
26 required and instead, as described above, after the wafer
27 40 is covered with the standard phosphorus-doped glass
28 layer 32, it is then covered by a thick extra layer of
29 silicon nitride insulation 34. The wafer is then covered
30 with a layer of photoresist 48, and holes 50 are opened
31 above each pad 44 (as suggested above, there are two for
32 each die) to expose the standard contact pads 44.

33 After the pads 44 are open, the field metal layers 36
34 and 38 are deposited, typically using a DC-diode
35 sputtering process. Thereafter, a second layer 52 of
36 photoresist of about 30 microns thickness is deposited
37 over the wafer, and openings 54 are formed therein to
38 define the boundaries of the enhanced pads 22 and 24.
39 Subsequently the 25-micron thick enhanced pads 22 and 24

1 are electroplated onto the field metal. The photoresist
2 is then removed, and the dies are severed from the wafer
3 to yield the structure illustrated in Fig. 2. It will be
4 understood of course that testing can be accomplished
5 either before the dies are separated or afterwards.

6 Thereafter the die may be bonded to a support plate
7 18, as depicted in Fig. 1, and the leads 14 and 16 are
8 attached to the enhanced pads 22 and 24 respectively, by
9 soldering, thermal compression bonding or welding. It
10 will be appreciated that, although the wire attachment
11 operation is made directly to a component part of the die,
12 i.e. the enhanced pads 24 and 26 rather than to
13 leadframes, as described in the previously mentioned
14 copending application, the underlying circuit is protected
15 by a combination of the layer 34 and the structural
16 characteristics of the pads 24 and 26.

17 In Fig. 4 an encapsulated transponder device is
18 generally indicated as 60. Numerals 10, 12, 14 and 16
19 refer to the corresponding elements of the transponder
20 referred to in the previous figures. The transponder is
21 located within a tube 62 which has been preformed with one
22 end closed at 64. Insertion of the transponder into the
23 tube 62 can be accomplished by any suitable mechanical
24 placing means.

25 The tube 62 is made of a heat shrinkable material
26 such as heat shrinkable polyvinylidene fluoride. In
27 addition, the inside surface of the tube 62 may be
28 partially or fully coated with a thermal plastic 66. In
29 the event that the encapsulated transponder is to be
30 inserted into a living body, the heat shrinkable material
31 would need to be inert or further coated by an inert
32 material.

33 Once the transponder is positioned within the tube 62
34 heat is applied to the tubing which then shrinks around
35 and protects the transponder. The thermal plastic 66,
36 when subjected to heat, softens and flows to seal the open
37 end 68 of the tube. As a result of this procedure, the
38 transponder is encapsulated within a rigid and durable
39 plastic container and is entirely sealed off, partially by

1 the thermal plastic 66 and partially by the heat shrunk
2 tube 62, from adverse environmental influences. The post
3 shrinking format of the tube and thermal plastic is shown
4 in broken lines in Fig. 4.

5 Although the use of the heat shrink plastic is known
6 for encapsulating electronic devices (see United States
7 Patents 3,603,720 and 4,695,926), the prior art
8 encapsulated devices have leads protruding therefrom which
9 makes hermetic encapsulation very difficult. The
10 transponder of the invention has no leads protruding
11 beyond the outer extremities of the tube 62 and this
12 feature together with the use of a thermal plastic makes
13 this advantageous encapsulation of the transponder a non-
14 obvious application of the heat shrinking plastics
15 technology. Turning now to Fig. 5 it will be seen that
16 the transponder in this case is located within a glass
17 capsule 26 very much the same as illustrated in Fig. 1.
18 In this figure, the transponder together with the capsule
19 has been inserted within a heat shrinkable tube 62. As
20 illustrated, the tube 62 has already been shrunk and the
21 thermal plastic has flowed to form a plug 66 at what was
22 the open end of the tube 62. The choice of encapsulating
23 the transponder in only a heat shrink plastic or a
24 combination of glass capsule and heat shrink plastic will
25 depend on the specific uses that the device needs to be
26 put to.

27 The advantage of using this method of protecting the
28 transponder lies in that it is technically complex to use
29 conventional plastic molding techniques to mold a plastic
30 envelope around the transponder. This is because it is
31 difficult to hold the transponder in the center of the
32 mold cavity, which is necessary to achieve an adequate
33 plastic wall thickness about the transponder. Insertion
34 of the transponder into a preferred heat shrinkable
35 plastics tube avoids these difficulties.

36 Furthermore, the transponder device referred to in
37 this description could be as small as 10 mm in length and
38 1.5 mm in diameter. This very small size makes the
39 transponder extremely delicate and difficult to work with

1 and effectively prevents the use of any encapsulation
2 techniques that will damage it. The method of this
3 invention is very well suited to encapsulation of a
4 transponder such as this.

5

6 Although the present invention has been described in
7 terms of an embodiment particularly suited for use in the
8 fabrication of a microtransponder device, the same process
9 can be used to make devices for a wide variety of
10 applications.

11 Furthermore, although the present invention has been
12 described above in terms of a single preferred embodiment,
13 it is anticipated that numerous alterations and
14 modifications thereof will become apparent to those
15 skilled in the art. It is therefore intended that the
16 appended claims be interpreted broadly as covering all
17 such alterations and modifications as fall within the true
18 spirit and scope of the invention.

19 What is claimed is:

CLAIMS

1 1. A method of providing enhanced contact pads to an
2 integrated circuit device forming a part of an
3 encapsulated miniature transponder device, comprising:
4 depositing an additional layer of insulating material
5 over the surface of the device;
6 opening apertures in said insulative layer to expose
7 the standard circuit contact pads of the device; and
8 forming enhanced contact pads overlying said
9 insulative layer and communicating with said standard
10 contact pads through said apertures to provide a die
11 device to which electrical interconnect leads may be
12 directly connected.

1 2. A method as recited in claim 1 wherein said enhanced
2 contact pads are formed by first depositing field metal in
3 electrical connection with said standard pads and plating
4 said enhanced pads directly thereover.

1 3. A method as recited in claim 2 wherein said
2 additional insulative layer is a layer of silicon nitride
3 having a thickness in excess of 10,000 angstroms.

1 4. A method as recited in claim 3 wherein said enhanced
2 pads are made of a metal selected from the group
3 consisting of gold or copper and have a thickness of at
4 least 20 microns.

1 5. An integrated circuit device forming a part of an
2 encapsulated miniature transponder device, comprising:
3 a silicon substrate forming a die having an
4 integrated circuit formed in a surface thereof and
5 including a metallization layer forming a first set of
6 contact pads;
7 an insulative layer covering the surface of said die
8 including said metallization layer and having apertures
9 therein exposing said first contact pads; and

10 a plurality of second contact pads disposed over said
11 insulative layer and contacting said first pads through
12 said openings.

1 6. An integrated circuit device as recited in claim 5
2 wherein said insulative layer has a thickness in excess of
3 10,000 angstroms.

1 7. An integrated circuit device as recited in claim 6
2 wherein the thickness of said second pads is in excess of
3 20 microns.

1 8. An integrated circuit device as recited in claim 7
2 wherein said integrated circuit device forms the signal
3 generating circuitry of the transponder device.

1 9. An integrated circuit device as recited in claim 8
2 and further comprising means forming an electromagnetic
3 antenna having wire leads bonded to said second pads.

1 10. An integrated circuit device as recited in any one of
2 claims 5 to 9 further comprising encapsulating means
3 commonly encapsulating said die and said antenna to form
4 a transponder device.

1 11. An integrated circuit device as recited in claim 10
2 wherein the encapsulating means comprises a heat shrunk
3 plastics material.

1 12. An integrated circuit device as recited in claim 11
2 wherein the plastics material is inert and suitable for
3 use in a live body.

1 13. An integrated circuit device as recited in claim 12
2 wherein the encapsulating means includes a glass capsule
3 about which the heat shrunk plastics material is formed.

- 1 14. A leadless passive transponder comprising:
2 a signal generator, a signal transmitter, and a
3 coupling means for inductive coupling to a force field so
4 that variation of the force field relative to the coupling
5 means generates an electric current within at least the
6 generator;
7 wherein the entire transponder is encapsulated in a
8 heat shrunk material.
- 1 15. A transponder as recited in claim 14 wherein the
2 transponder is first encapsulated in a glass capsule and
3 thereafter in the heat shrunk material.
- 1 16. A transponder as recited in either claim 14 or claim
2 15 wherein the heat shrunk material is, before being
3 applied to encapsulate the transponder, in the form of a
4 hollow tube which is closed at one end.
- 1 17. A transponder as recited in claim 16 wherein the tube
2 includes a thermal plastic which when heat is applied to
3 the tube flows to seal the open end of the tube.
- 1 18. A method of encapsulating a leadless passive
2 transponder, the transponder including a coupling means
3 for inductively coupling the transponder, when in use, to
4 a force field so that variation of the force field
5 relative to the transponder generates an electric circuit
6 within the transponder, a signal generator, and a signal
7 transmitter, the method comprising the steps of:
8 placing the transponder in an envelope of heat
9 shrinkable material; and
10 applying heat to the heat shrinkable material so as
11 to encapsulate the transponder within the envelope.
- 1 19. A method as recited in claim 18 wherein the envelope,
2 before the application of heat to the heat shrinkable
3 material, is in the form of a tube with one end thereof
4 closed off.

- 1 20. A method as recited in claim 19 wherein the tube
- 2 includes a thermal plastic which, when heat is applied,
- 3 flows to seal off the open end of the tube.

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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US92/04731

A. CLASSIFICATION OF SUBJECT MATTER

IPC(5) : G01S 13/80; H01L 21/447, 21/56, 23/06; H01Q 1/40

US CL : 340/572; 343/788, 873; 357/71.74; 437/189, 219; 606/116; 342/51

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : Please See Extra Sheet.

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

Please See Extra Sheet.

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category ^a	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
<u>X</u> Y	US, A, 4,273,859 (MONES ET AL.) 16 JUNE 1981 See Abstract, Fig. 3 and col. 5, lines 44-47.	<u>1-4</u> 5-12
Y	US, A, 4,992,794 (BROUWERS) 12 FEBRUARY 1992. See Abstract, Fig. 1, and col. 2, lines 3-18.	5-12, 14, 16-20
Y	US, A, 4,695,926 (MCDERMOTT) 22 SEPTEMBER 1987. See Abstract, Figs 1-3 and column 4, lines 52-62 and column 5, lines 6-16.	11, 12, 14, 16-20
Y	US, A, 4,733,289 (TSURUMARU) 22 MARCH 1988 See Figs 2, 3 and the Abstract.	5-12
A	US, A, 4,911,217 (DUNN ET AL.) 27 MARCH 1990 See Figures 4, 7 and 10.	5-12
A	WO, A, WO90/14736 (TROVAN LIMITED) 29 NOVEMBER 1990. See Figure 5.	1-20

☒ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

^a Special categories of cited documents:	^T Inter document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
^A document defining the general state of the art which is not considered to be part of particular relevance	^X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
^E earlier document published on or after the international filing date	^Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
^L document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	^Z document member of the same patent family
^O document referring to an oral disclosure, use, exhibition or other means	
^P document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search 02 SEPTEMBER 1992	Date of mailing of the international search report 12 NOV 1992
Name and mailing address of the ISA/ Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231	Authorized officer PETER TOBY BROWN
Facsimile No. NOT APPLICABLE	Telephone No. (703) 308-4918

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US92/04731

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO, A, WO86/00498 (PAALMAN) 30 JANUARY 1986 See Figures 1 and 2 and column 5, line 32 - column 6, line 13.	1-20

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US92/04731

B. FIELDS SEARCHED

Minimum documentation searched

Classification System: U.S.

340/572; 343/788, 873; 357/71; 437/189, 219; 606/116; 324/51

340/573, 825.34, 825.54; 343/787; 437/213; 606/117; H01Q 7/00, 7/06, 7/08, 1/40.

B. FIELDS SEARCHED

Electronic data bases consulted (Name of data base and where practicable terms used):

US Automated Patented System files USPAT and SPOABS Silicon, Transponder#, Antenna#, C01L#, Ferrite, Core Bond###, Pad#, Contact#, Substrate, Wire#, VLSI, Lead#, Encapsulat###, Plastic#, Animal#, Circuit#, Chip#.

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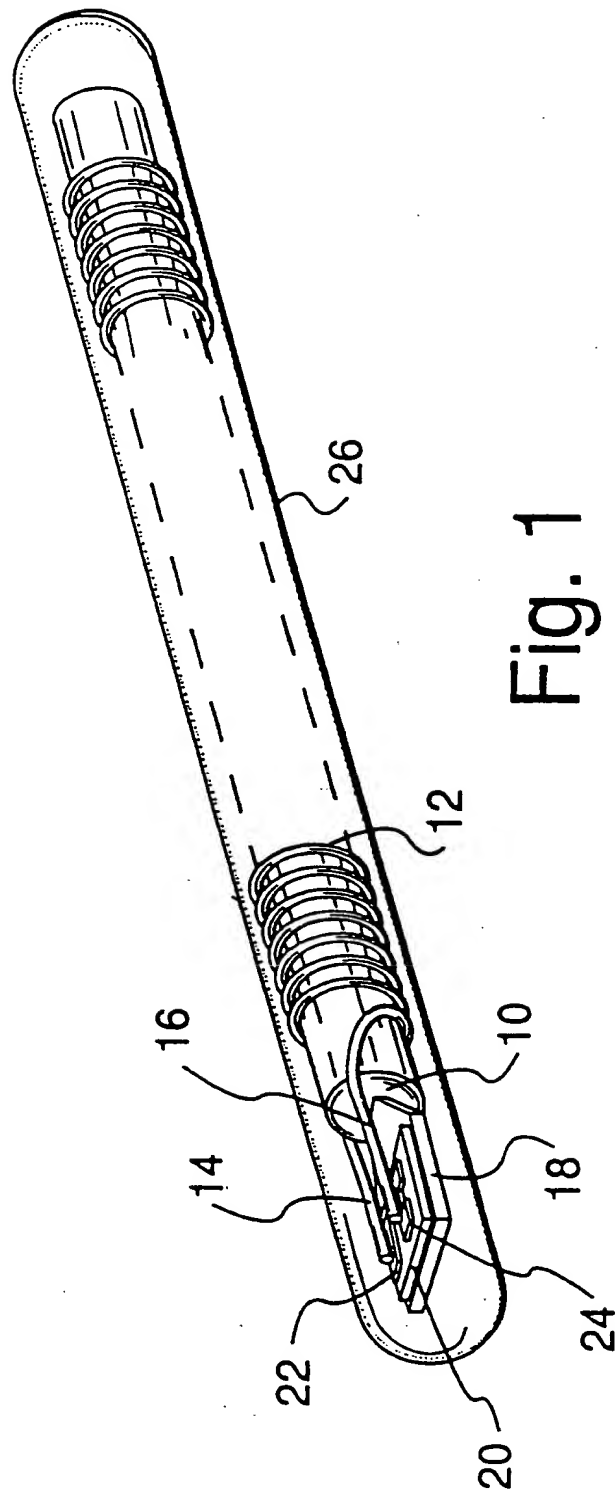


Fig. 1

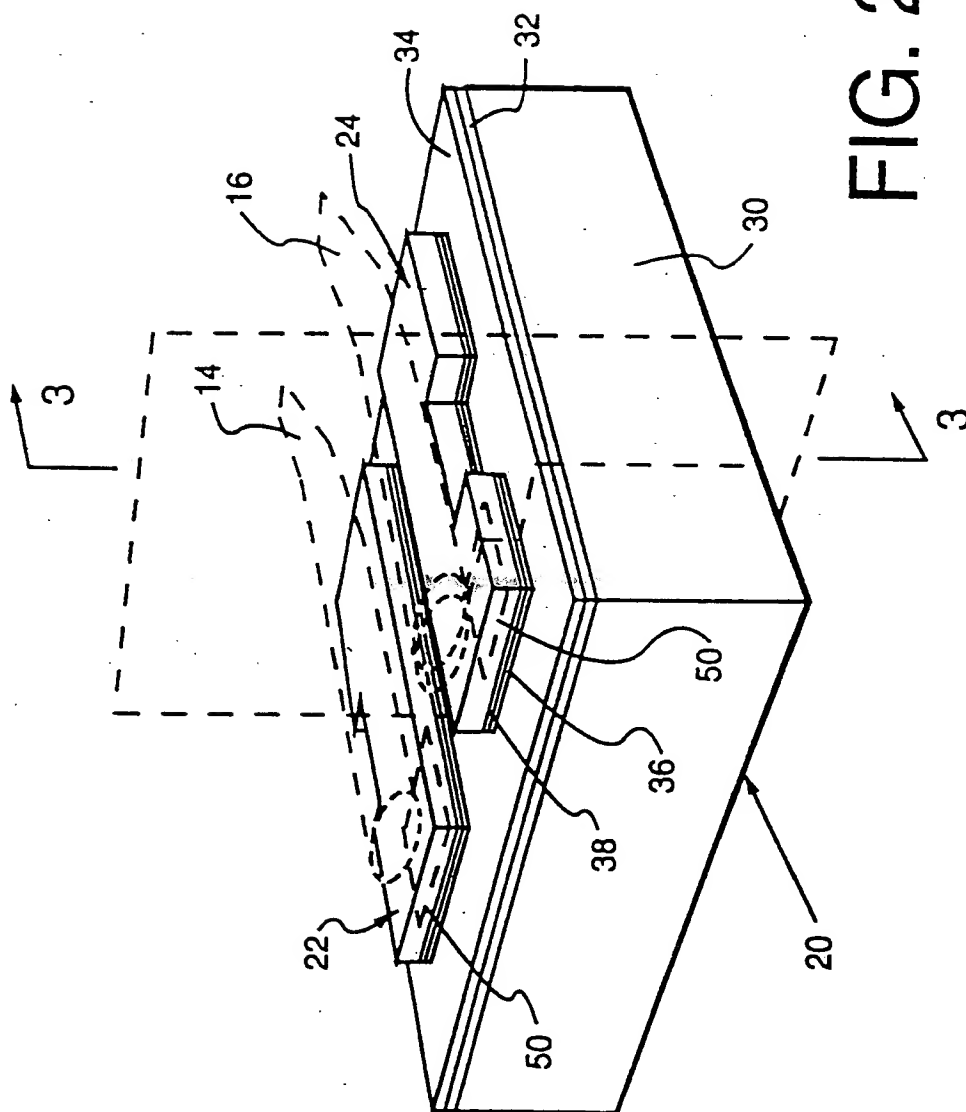


FIG. 2

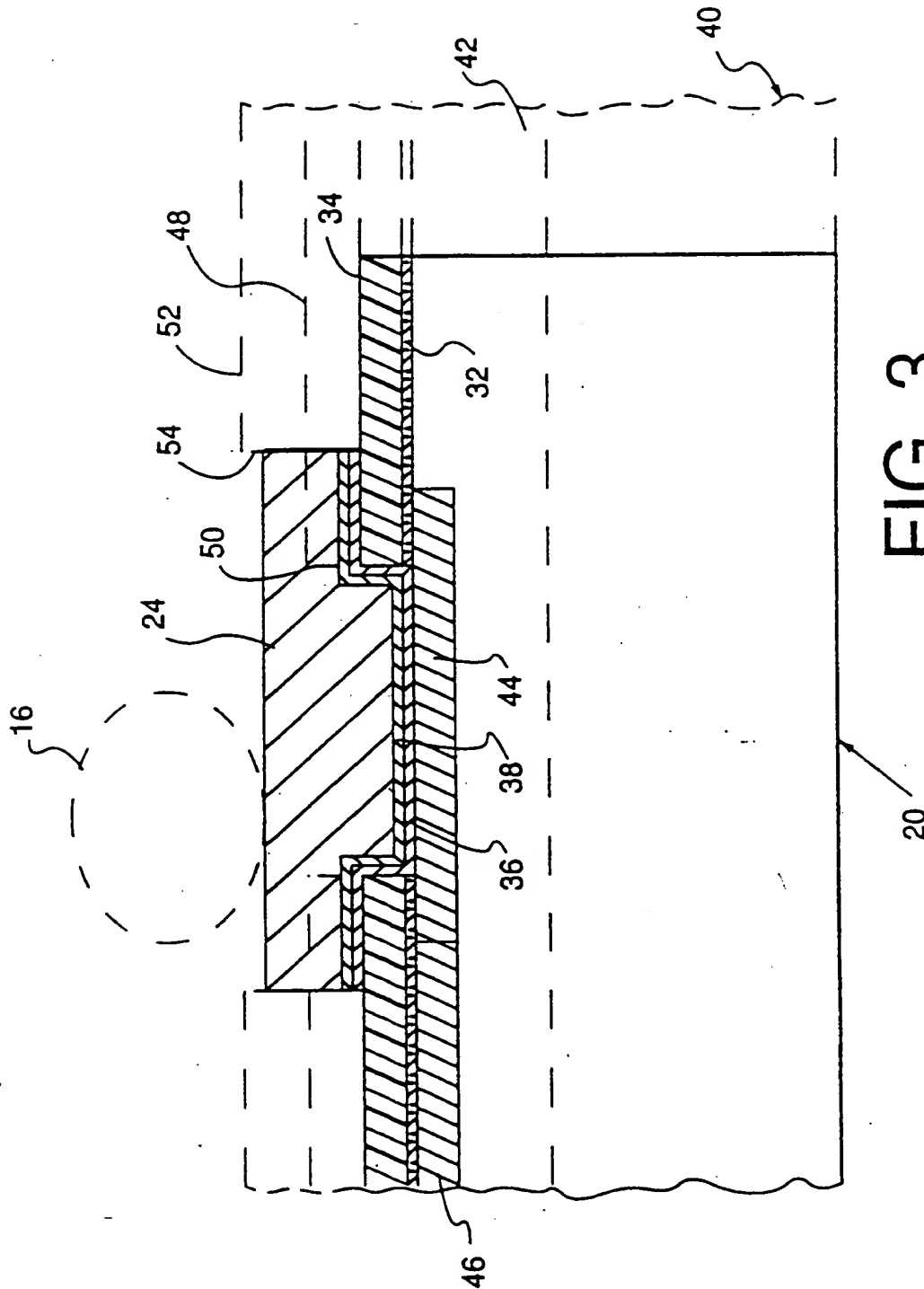


FIG. 3

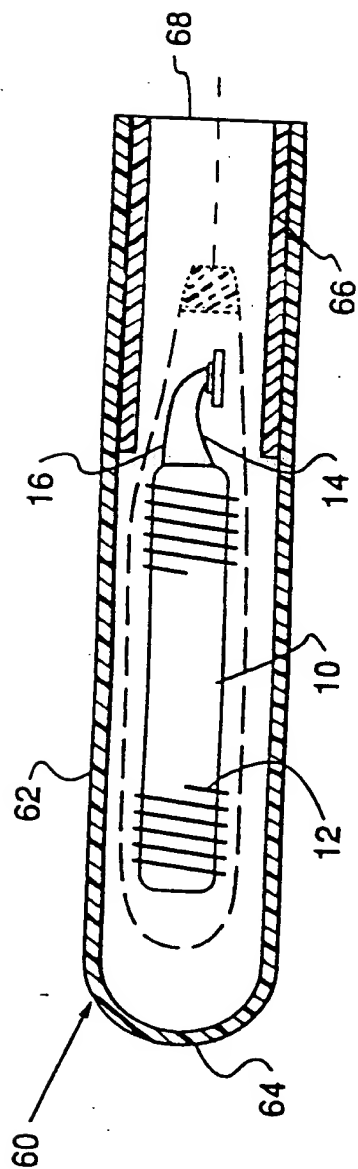


FIG. 4

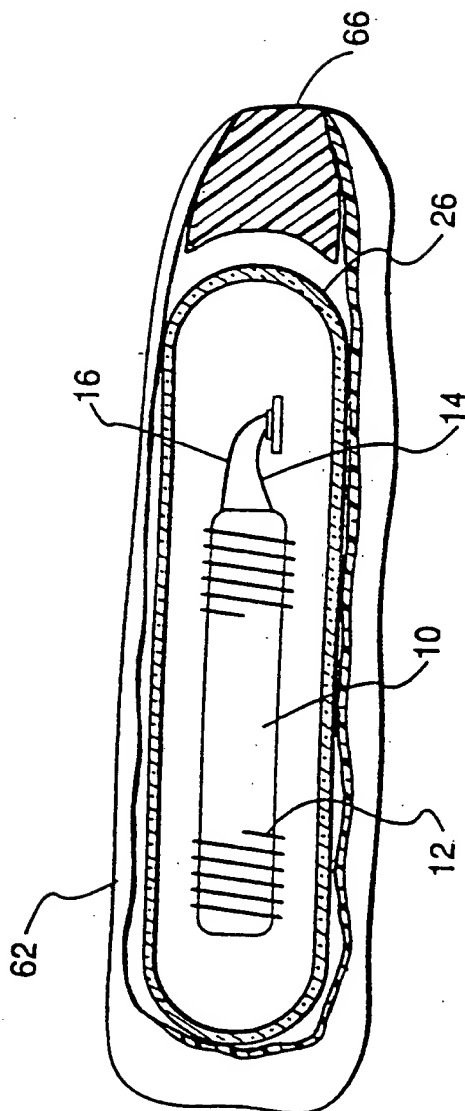


FIG. 5